

FIG. 1
(PRIOR ART)

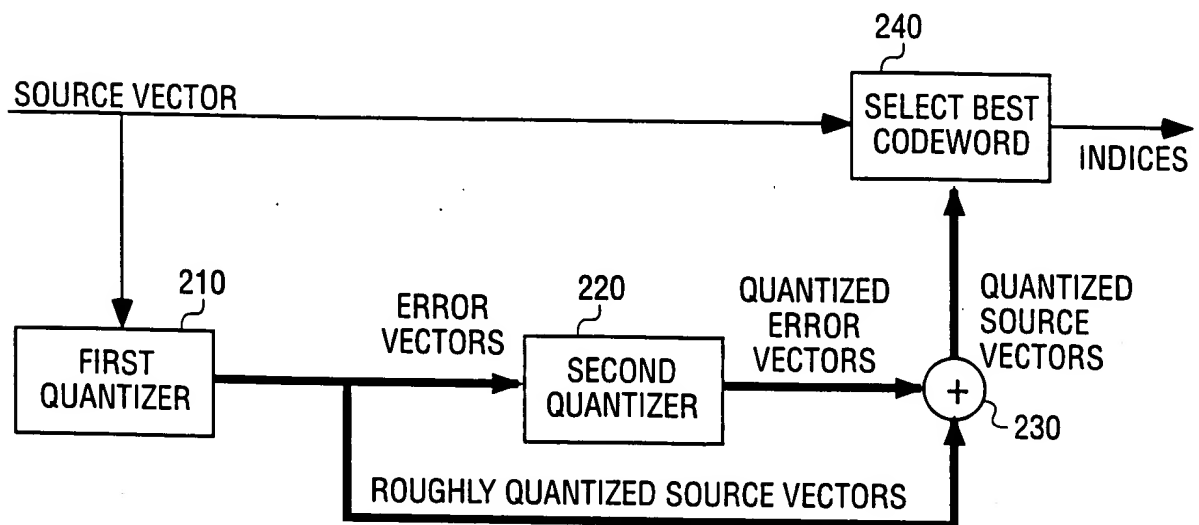


FIG. 2
(PRIOR ART)

300

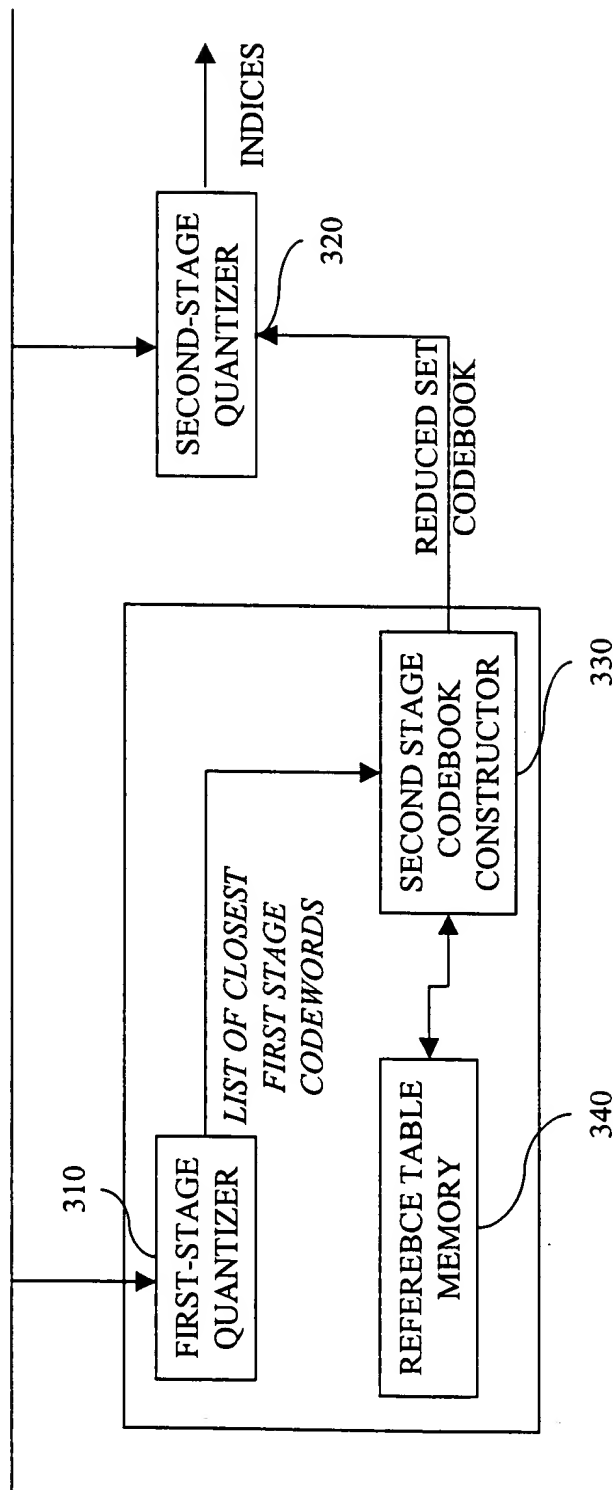


Fig. 3

FIG. 4 is a block diagram of a system 400 for processing an input signal. The system 400 includes a quantizer 405 and a non-structured codebook C 402. The input signal is provided to the quantizer 405 and the non-structured codebook C 402. The non-structured codebook C 402 outputs a reduced set codebook to the quantizer 405.

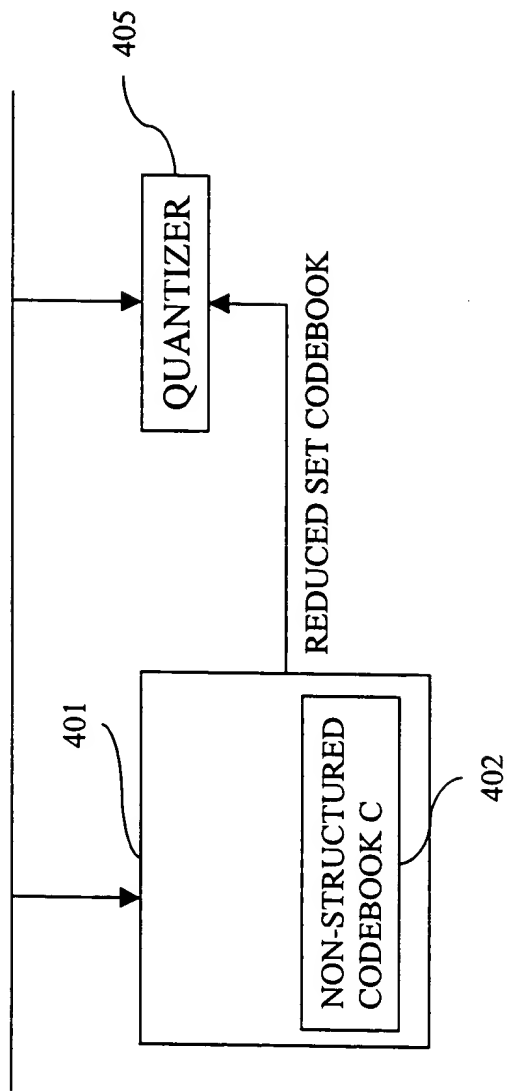


Fig. 4

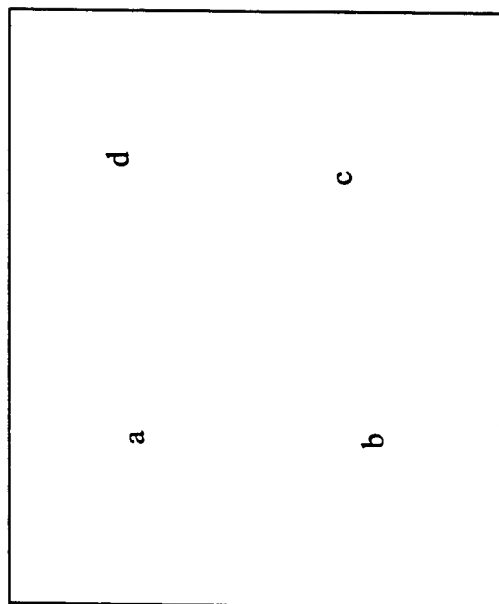
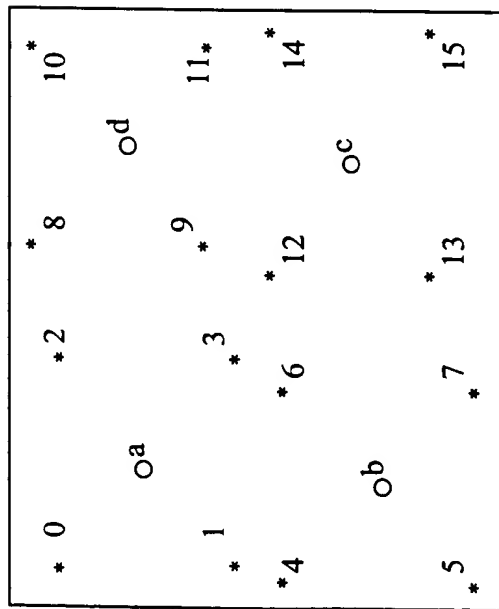


Fig. 5A

Fig. 5B

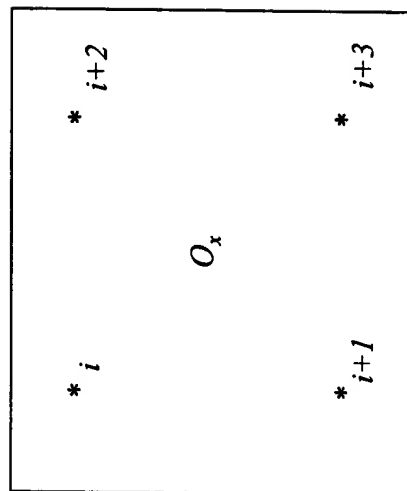


Fig. 5C

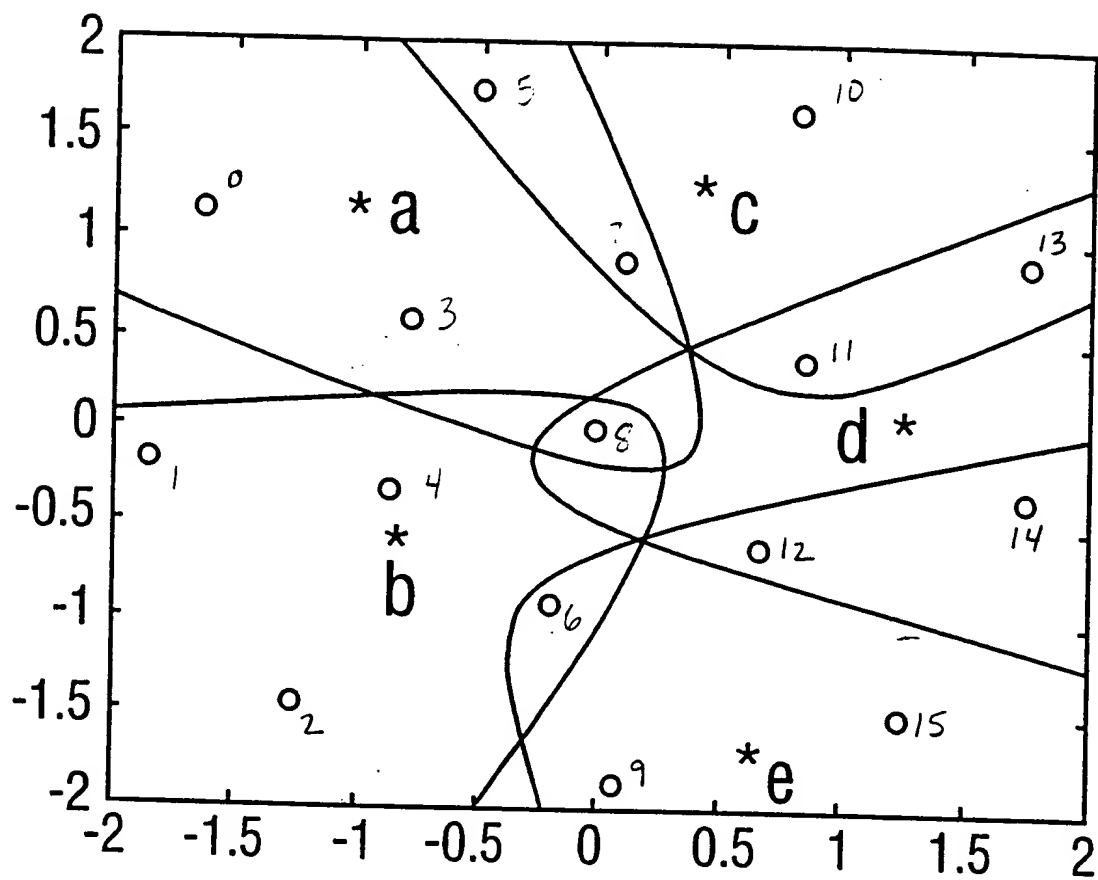


Fig. 6

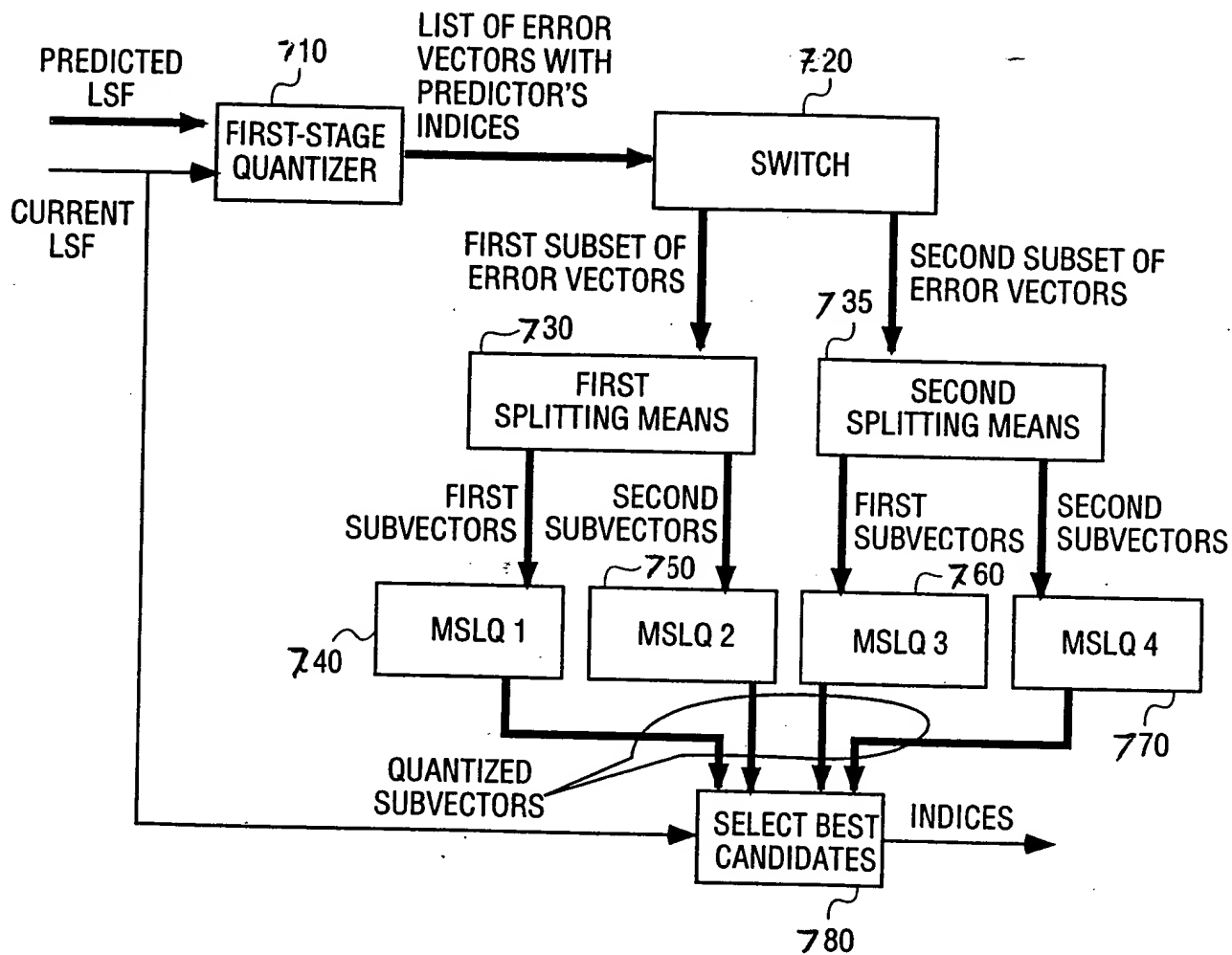
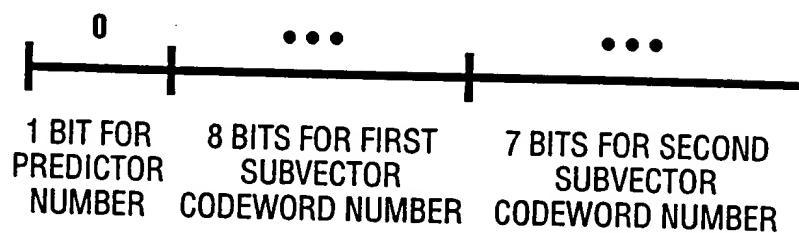


Fig. 7

EXAMPLE OF BIT ALLOCATION FOR 16 BITS PER LSF VECTOR CODING, $N=3$, $M_1=256$, $M_2=M_3$, $M_4=128$.

A) IF FIRST PREDICTOR IS THE BEST:



B) IF 2D OR 3D PREDICTOR IS THE BEST:

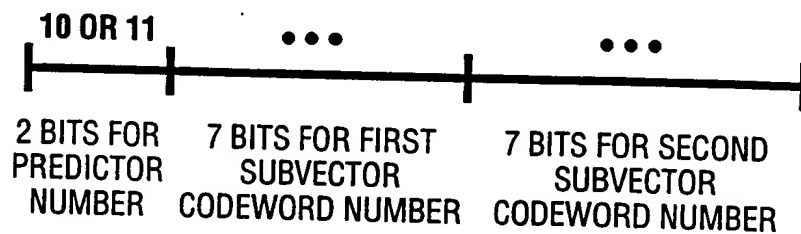


Fig. 8